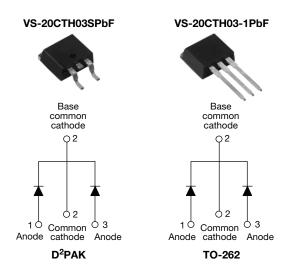


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HALOGEN

FREE

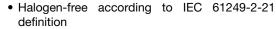
Hyperfast Rectifier, 2 x 10 A FRED Pt®

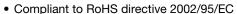


PRODUCT SUMMARY				
t _{rr} (maximum)	35 ns			
I _{F(AV)}	2 x 10 A			
V_{R}	300 V			

FEATURES

- Hyperfast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C





• AEC-Q101 qualified



Vishay HPP's 300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage		V_{RRM}		300	V
A	per diode	IF(AV)	T _C = 160 °C	10	
Average rectified forward current	per device			20	Α
Non-repetitive peak surge current		I _{FSM}	T _J = 25 °C	120	
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	300	-	-	
Forward voltage	VF	I _F = 10 A	-	1.05	1.25	V
Torward voltage		I _F = 10 A, T _J = 125 °C	-	0.85	0.95	
Poverse leekage ourrent	_	$V_R = V_R$ rated	-	-	20	
Reverse leakage current I _R		T _J = 125 °C, V _R = V _R rated	=	6	200	μΑ
Junction capacitance	C _T	V _R = 300 V	-	30	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body -		8	-	nH

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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Bernard		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R$		1	-	35	
		$I_F = 1.0 \text{ A}, dI_F/dt =$	100 A/ μ s, $V_R = 30 V$	-	-	30	ns ns
Reverse recovery time	t _{rr}	T _J = 25 °C		-	31	-	
		T _J = 125 °C		-	42	-	
Peak recovery current I _{RRM}		T _J = 25 °C	$I_F = 10 \text{ A}$	-	2.4	-	Α
	IRRM		-	5.6	-		
Reverse recovery charge	0	T _J = 25 °C	,,,	=	36	=	nC
	Q _{rr}	T _J = 125 °C		-	120	-	110

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per diode	R _{thJC}		-	-	1.5	°C/W
Maralia			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0	_	12	kgf · cm
Woulding torque			(5.0)	_	(10)	(lbf · in)
Marking device		Case style D ² PAK	20CTH03S			
Marking device		Case style TO-262	20CTH03-1			

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For technical questions, contact: diodestech@vishay.com

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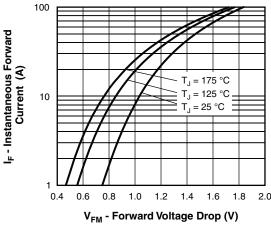


Fig. 1 - Maximum Forward Voltage Drop Characteristics

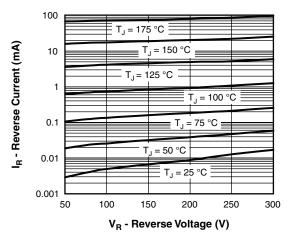


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

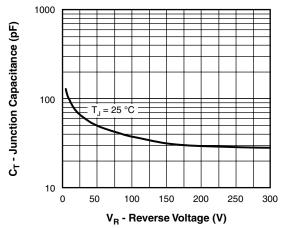


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

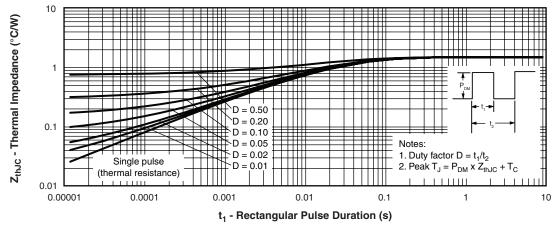


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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t_{rr} (ns)

100

10

1000

100



1000

T_J = 125 °C

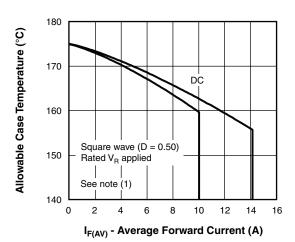
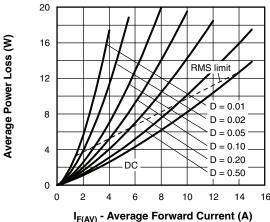


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



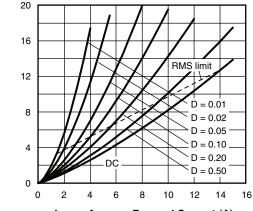
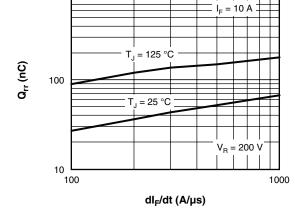


Fig. 6 - Forward Power Loss Characteristics



T_J = 25 °C

dl_F/dt (A/µs) Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

V_R = 200 V

Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$

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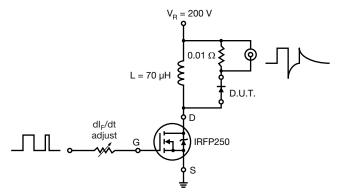
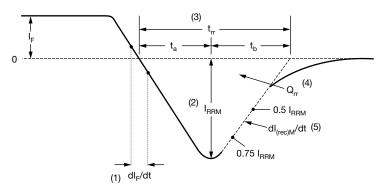


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_F$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

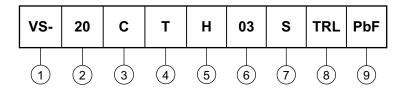
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ORDERING INFORMATION TABLE

Device code



- 1 HPP product suffix
- 2 Current rating (20 A)
- 3 C = Common cathode
- 4 T = TO-220, D²PAK
- 5 H = Hyperfast rectifier
- 6 Voltage rating (03 = 300 V)
- 7 • S = D²PAK
 - -1 = TO-262
- None = Tube (50 pieces)
 - TRL = Tape and reel (left oriented, for D²PAK package)
 - TRR = Tape and reel (right oriented, for D²PAK package)
- 9 PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95014</u>					
Part marking information	www.vishay.com/doc?95008				
Packaging information	www.vishay.com/doc?95032				

For technical questions, contact: diodestech@vishay.com

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Revision: 18-Jul-08

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